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JIANQ CHYUN IPO

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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/605,012	09/01/2003	CHIH-PIN HUNG	10320-US-PA	2011

TITLE OF INVENTION: [STRUCTURE OF FLIP CHIP PACKAGE WITH AREA BUMP]

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1330-1370	\$300	\$1630-1670	10/22/2004

EXAMINER	ART UNIT	CLASS-SUBCLASS
ROMAN, ANGEL	2812	257-778000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

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3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE

(B) RESIDENCE (CITY and STATE OR COUNTRY)

Advanced Semiconductor Engineering, Inc. Kaohsiung, Taiwan, R.O.C.

Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ individual ☒ corporation or other private group entity ☐ government

4a. The following fee(s) are enclosed:

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☐ a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27.☐ b. Applicant is not claiming SMALL ENTITY status. See, e.g., 37 CFR 1.27(g)(2).

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PAGE 2/5 * RCVD AT 10/21/2004 9:41:15 PM [Eastern Daylight Time] * SVR:USPTO-EFXXRF-2/0 * DNIS:7464000 * CSID:886 2 23697233 * DURATION (mm-ss):02-06

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02 FC:1504 300.00 DA

03 FC:8001 9.00 DA